

프로그래머블 무선 메쉬 네트워크  
테스트베드 및 가상화

이준희, 이경한, 이진성, **Sachin Lal. Shrestha**, 정재성, **and** 정송

School of EECS, KAIST

{junhee,khan,ljs,sachinls,yicdk}@netsys.kaist.ac.kr,  
song@ee.kaist.ac.kr

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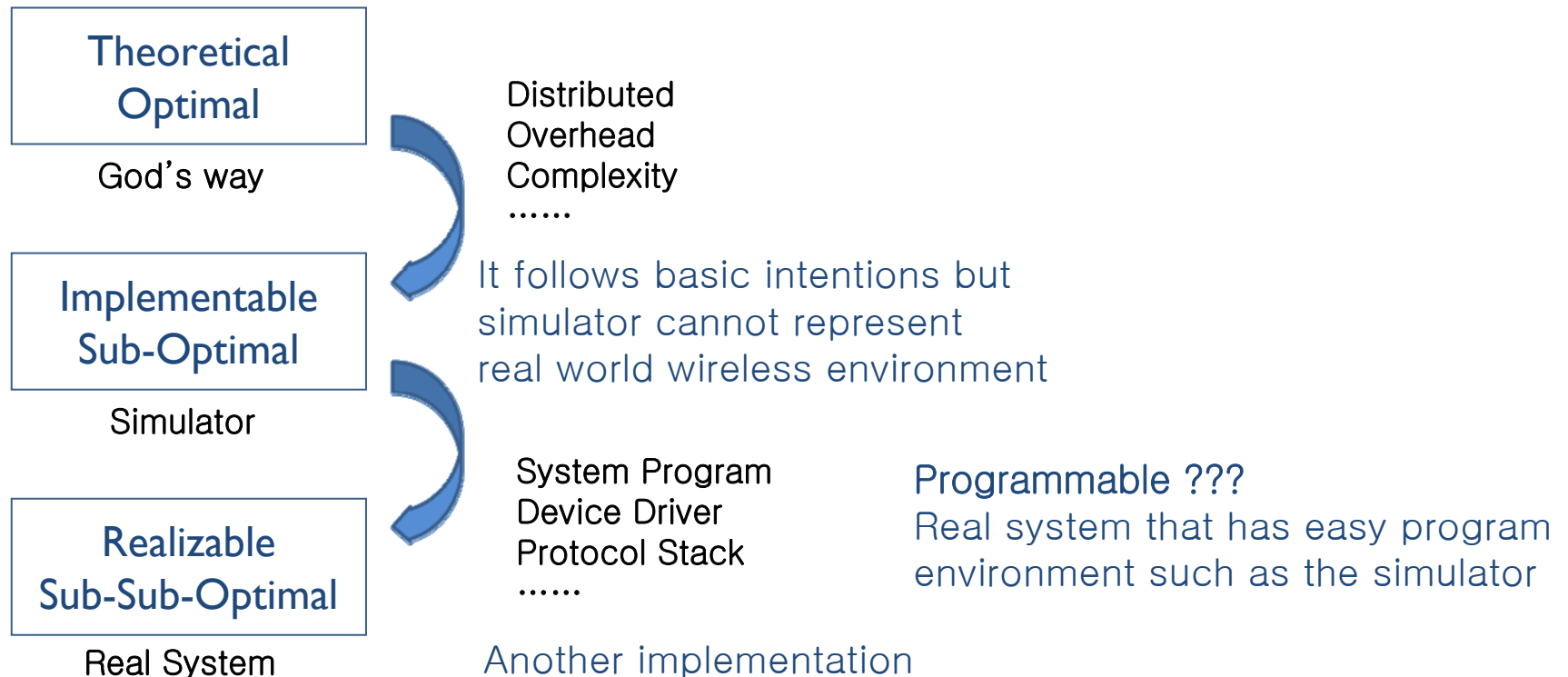
# Introduction [1/2]

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- ▶ Internet Clean-Slate Design [\[Feldmann07\]](#)[\[Dovrolis08\]](#)
  - ▶ Redesign from scratch
    - ✓ Clean-slate thinking
    - ✓ Evaluation of a clean-slate attempt
    - ✓ **Experimentation using testbed**
    - ✓ Deployment of clean-slate ideas
  - ▶ Clean-Slate Design for Wireless Mesh Networks
    - ▶ Known theoretical optimal solution [\[Tiss92\]](#)
      - ✓ Source/Link algorithm
    - ▶ Cross-layer design
      - ✓ Far from Internet layered architecture
      - ✓ Implementation is not easy

# Introduction [2/2]

## ▶ Programmable Wireless Mesh Network Testbed



# Previous Work

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## ▶ Protocol Design and Evaluation [Korff06]

### Simulator

(Serialized processing, using simulation clock)

NS-2/NS-3, JiST/SWANS, GloMoSim, QualNet, OPNET  
[NS2][NS3] [Barr04] [GloMoSim] [QualNet] [OPNET]

### Emulator

(Real application, simultaneous events, real system clock)

Judd(DSP/FPGA), JiST/MobNet, ORBIT(802.11Radio), <Click Modular>  
[Judd04] [Krop07] [Raych05] [Kohler00]

### Real World Testbed

(Real wireless channel without modification)

projects)  
WiMesh, RoofNet, TrueMobile, <PlanetLab>  
[WiMesh] [RoofNet] [Johnson05] [Peter02]

# Concerns of Previous Work

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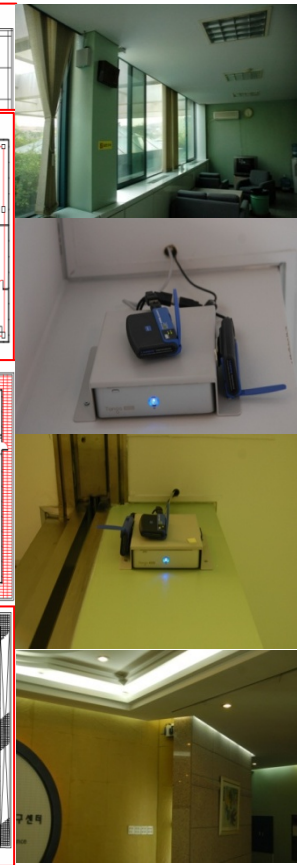
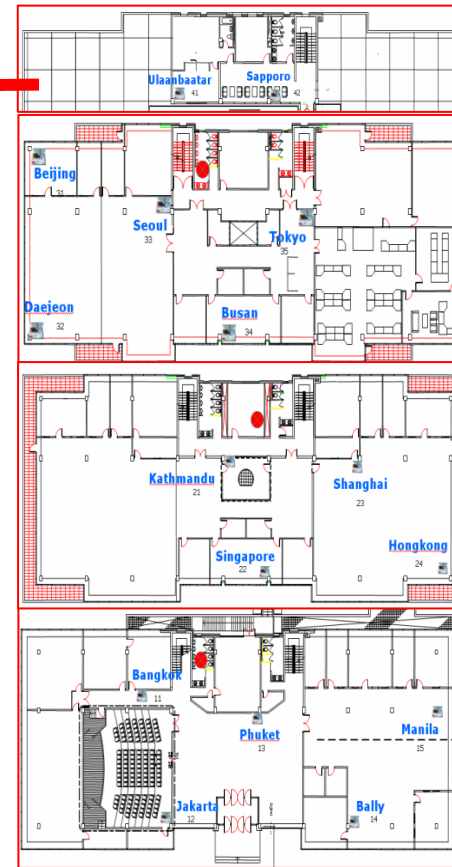
- ▶ Do they have concerns about implementation of clean-slate ideas ?
  - ▶ Simulator
    - ✓ Probably “Yes”
  - ▶ What about emulator & testbed ?

Emulator	Testbed
Accuracy (Fidelity) Scalability	Measurement Evaluation
Management, Monitoring, Friendly User Interface, Capacity	

- ✓ Limited programmability (because substrate is real thing)
- ▶ Programmable Platform in Wireless Network
  - ▶ OpenWRT, WARP, NS-3(Emulation), Jist/MobNet

# KAIST WiSEMesh Testbed Overview

- ▶ An Open Research Testbed ( <http://wisemesh.kaist.ac.kr> )
  - ▶ 56 Fixed Wireless Mesh Routers (Campus-scale testbed)



# KAIST WiSEMesh Platform

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## ▶ Architecture for Open Research

### ▶ Hardware platform

- ✓ Small form-factor PC
  - Pentium CM 1.4GHz
  - 512MB RAM, 60G HDD
  - Ralink x 3, Atheros x 1 (802.11b/g)

### ▶ Software platform

- ✓ Ubuntu Linux (Debian)
- ✓ All open source codes for protocols and drivers

### ▶ Management/Measurement

- ✓ Dedicated wired management channel
- ✓ Dedicated management server
- ✓ Visualization tool (WiVi) (<http://143.248.238.6/~sachin/realization/>)



# WiSEMesh Testbed Pros & Cons

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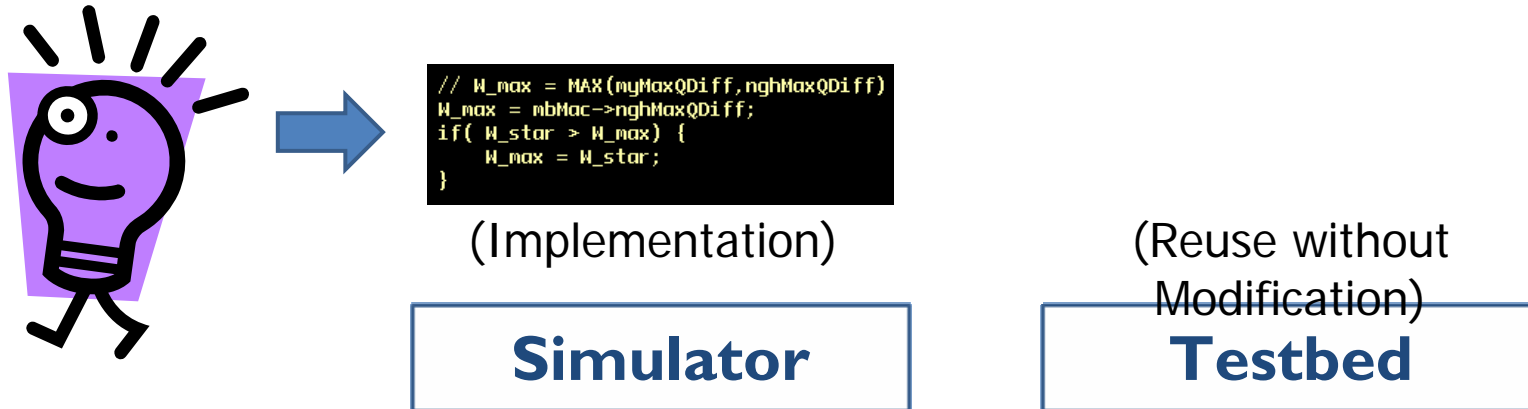
- ▶ Characteristics for Open Research
  - ▶ Open source architecture for open research
  - ▶ Remotely accessible, light management overhead
  - ▶ Automatic experiment configuration
  - ▶ Automatic measurement collection
  - ▶ Visualization of current network status
- ▶ Limitation
  - ▶ **Very difficult to implement one's idea on it**
    - ✓ Transport: TCP/UDP in protocol stack
    - ✓ Routing protocol: monolithic, closed, static and inflexible
    - ✓ Datalink layer: proprietary hardware
    - ✓ Cross-layer: Impossible

# Common Code Architecture

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- ▶ Common Code Base

- ▶ A set of protocols or algorithms to avoid re-implementation

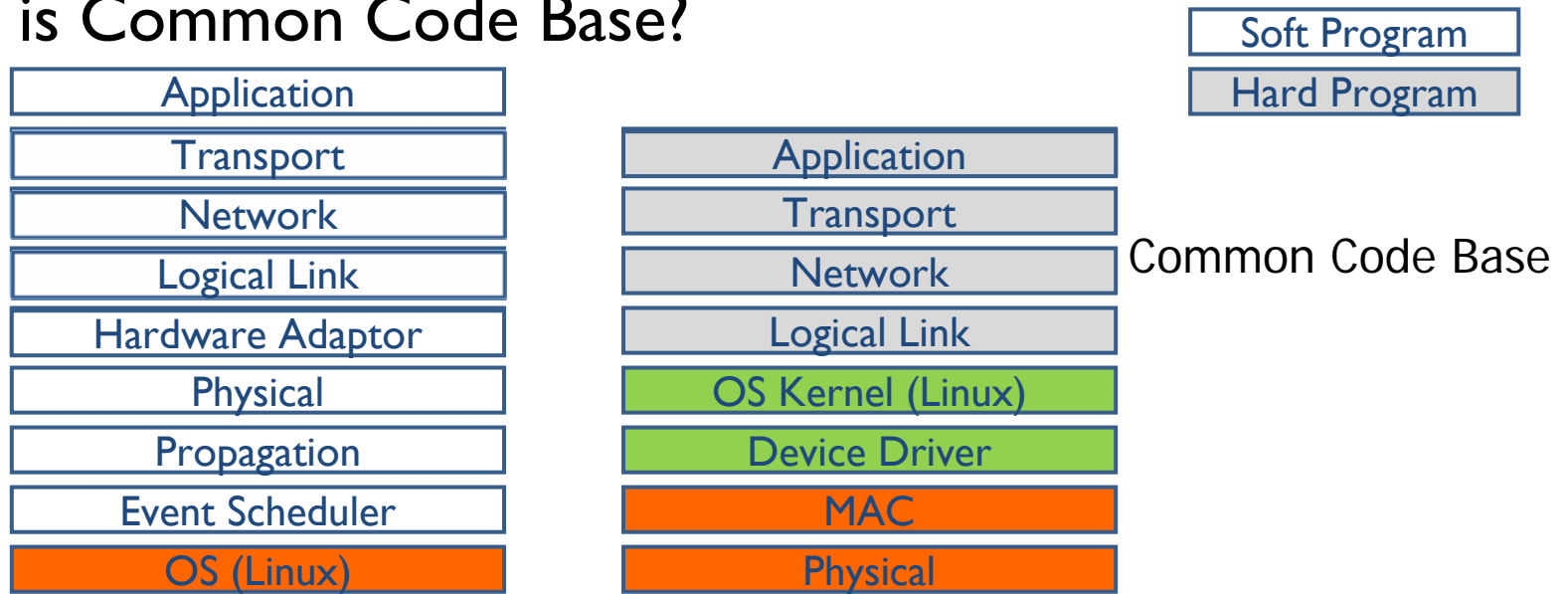


- ▶ Common Code Architecture

- ▶ Architecture that utilizes a Common Code Base
- ▶ A common set of metrics and tools for monitoring and analysis

# Simulator vs. Testbed Node

## ▶ What is Common Code Base?



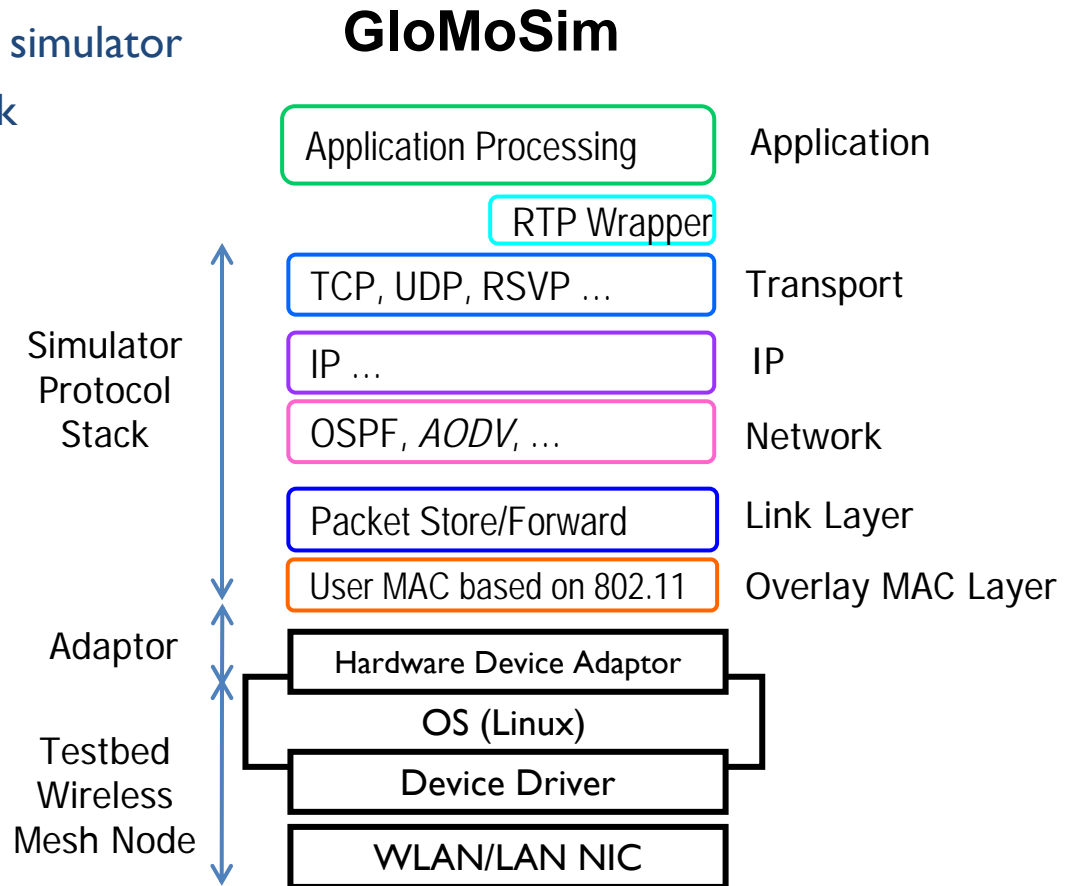
## ▶ Solution Simulator

Testbed Node

- ▶ With practiced simulator protocol stack
- ▶ Replace protocol stack of wireless mesh router with that of simulator
- ▶ Hardware adaptor for a real hardware
- ▶ Application adaptor for a real application

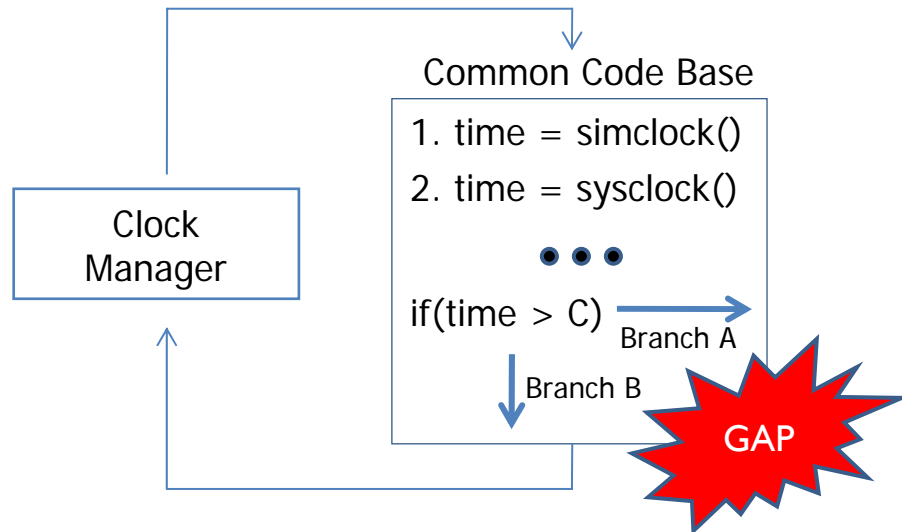
# Programmable Architecture

- ▶ **GloMoSim Simulator**
  - ▶ Well-known multi-hop wireless simulator
  - ▶ Designed for a wireless network
  - ▶ Custom C-language
  - ▶ Modular protocol component
  
- ▶ **Why Not NS-2**
  - ▶ Designed for wired network
  - ▶ Dual mode (C++/Otcl)
  
- ▶ **Adaptors for Emulation**
  - ▶ **Hardware device adaptor**
    - ✓ Raw socket program
      - Libnet driver
      - Libpcap driver



# Redesign of GloMoSim

- ▶ Step I: Parsec Compiler → C Compiler
  - ▶ Parsec is a C-based simulation language for parallel computing
  - ▶ Parsec library is not open source
- ▶ Step II: Simulation Clock → System Clock
  - ▶ Simulation clock can be stopped
  - ▶ System clock never be stopped



## Computing Resource Problem

$$\sum_{i \in E} p_i + \sum_{j \in B} p_j \leq \mathbf{P}$$

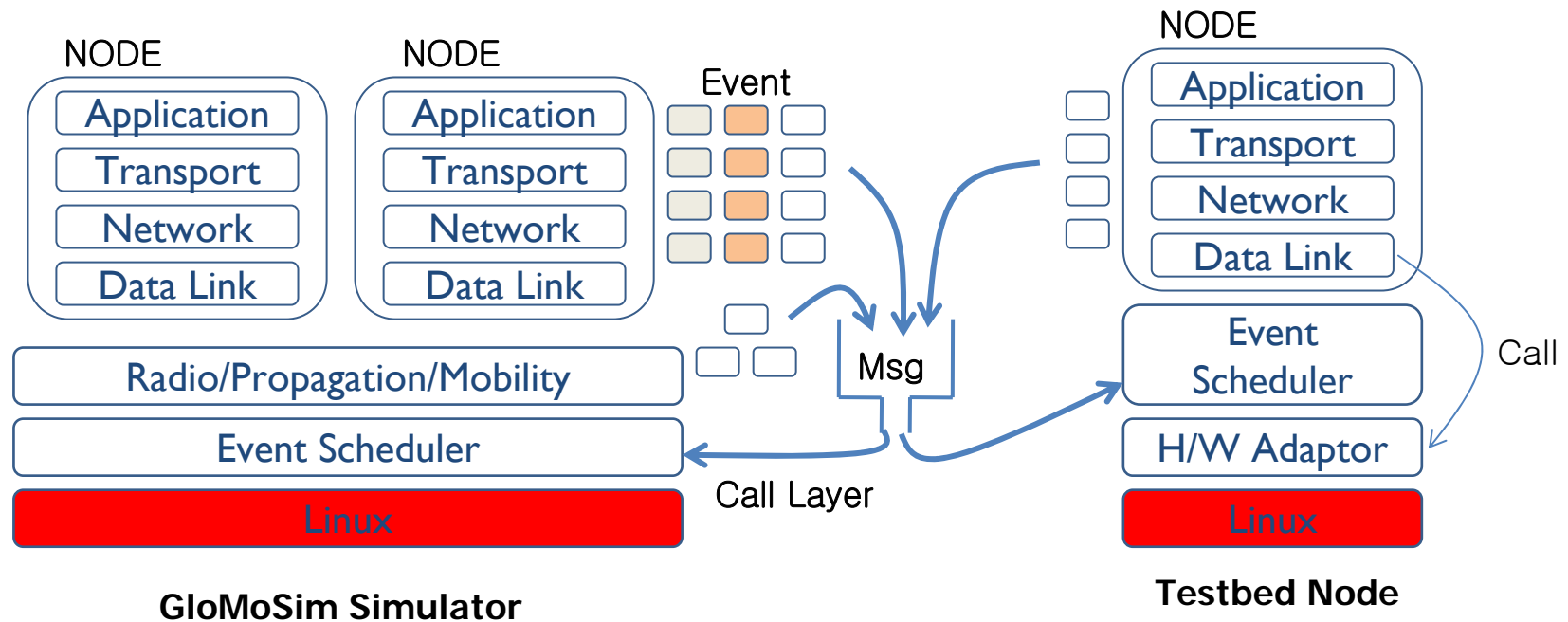
E: event set  
B: event set (bandwidth)  
p: computing power (cycle/sec)

## Causality Problem

```
if(time < Clocktarget) wait();
else schedule();
```

# Protocol Stack Transplantation

## ▶ Step III: Protocol Stack Transplantation

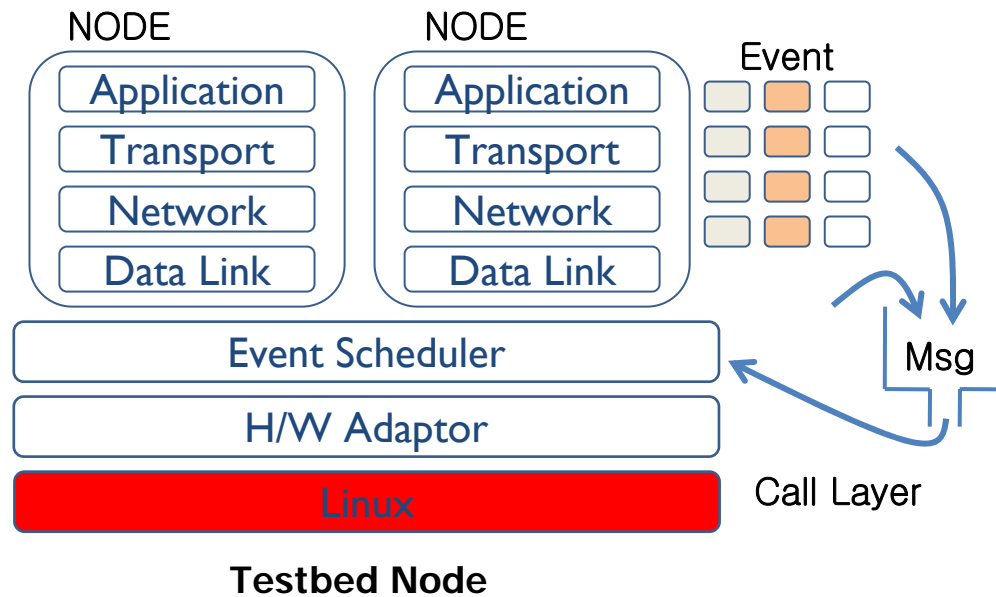


### ▶ Hardware device adaptor

- ✓ Interface with data link layer in protocol stack
- ✓ Interface with linux kernel (device driver)

# Virtualization

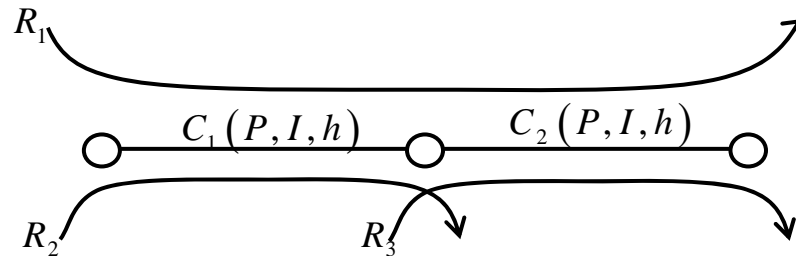
## ▶ User-level Virtualization



- ▶ Node virtualization by event scheduler
- ▶ Network slicing by H/W adaptor and daemon

# Theoretical Optimal Solution

## ▶ Multi-hop Wireless Networks



$P$ : power allocation  
 $I$ : link scheduling  
 $h$ : channel state

- ✓ Link capacity is time-varying and a function of resource control
- ✓ Joint rate, power allocation and link scheduling

## ▶ Long-term Network Utility Maximization [Tiss92][Chen06]

Flow Control at Source

$$\max_{R, P, I} \sum_s U(R_s) \quad \max_r \sum_s \left( U(r_s) - r_s \sum_{l \in L(s)} \lambda_l \right) \Rightarrow r_s = U'^{-1} \left( \sum_{l \in L(s)} \lambda_l \right)$$

s.t.

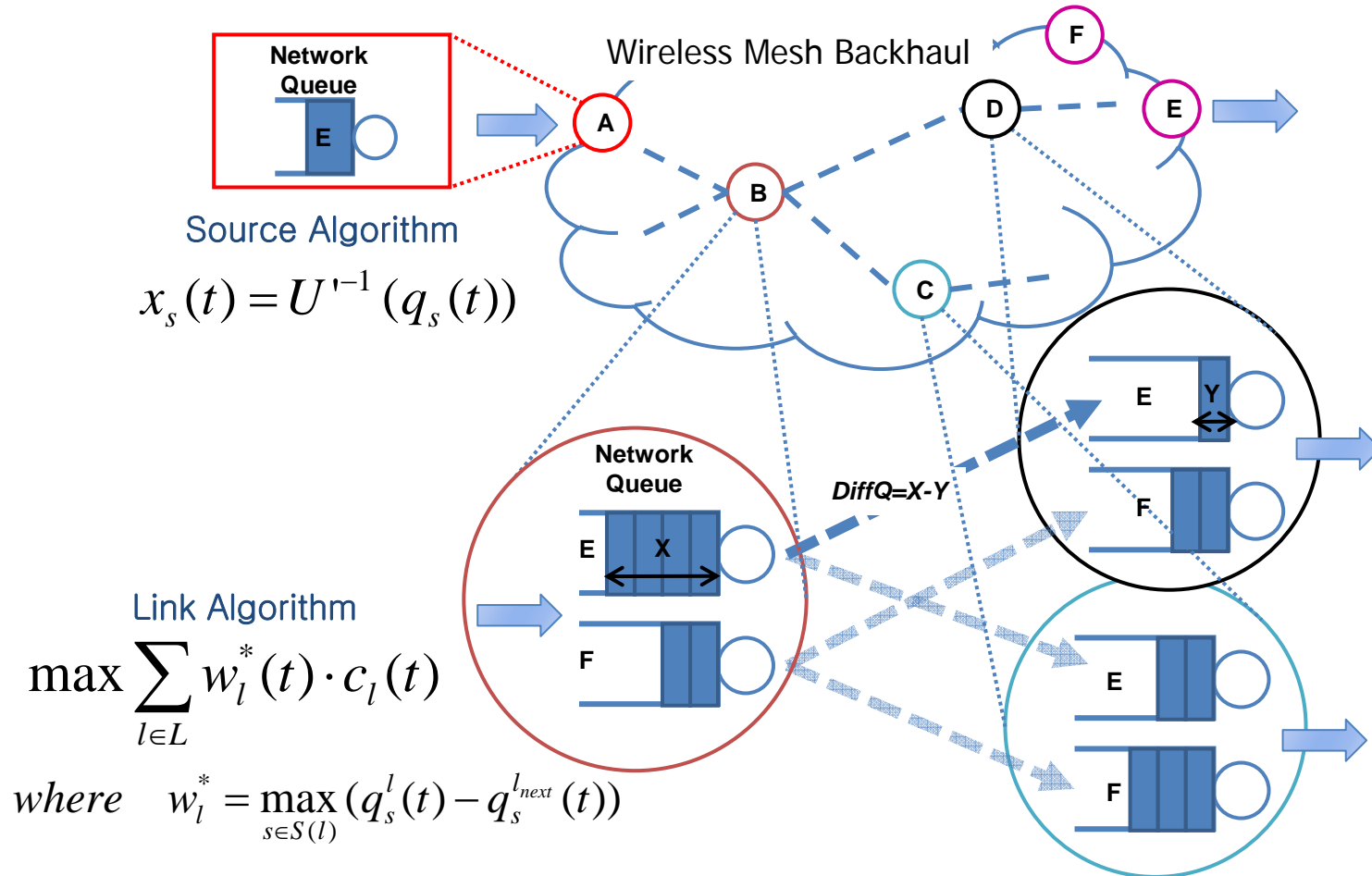
$$R \in F(P, I)$$

Scheduling/Power Control at Link

$$\max_{P, I} \sum_l \lambda_l C_l(P, I, h)$$

# Per-Destination Queueing Modeling

## Optimal Algorithm



# Solution Development for Implementation

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- ▶ **Highly Complicated Scheduling Scheme**
  - ▶ Applied to a simple network model
    - ✓ One-hop interference model
    - ✓ Symmetric, constant link capacity
  - ▶ Relaxation of the optimality requirements
    - ✓ Greedy Maximal Scheduling [Lin06][Leo01]
      - Feasible sub-optimal solution
      - Much lower complexity than that of optimal solution
      - Lower-bound is known as  $\frac{1}{2}$  of the optimal solution
        - Node-exclusive interference model
- ▶ **Development of Decentralized Algorithm**
  - ▶ Queue length exchanging method (piggyback, overhear)
  - ▶ The longest differential backlog serves the first
    - ✓ Mapping function from differential backlog to priorities

# Approximated Cross-layer Control

## ▶ Source Algorithm

$$x_s(t) = U'^{-1}(q_s(t))$$

### ▶ Rate control based on queue length

## ▶ Link Algorithm

$$\max \sum_{l \in L} w_l^*(t) \cdot c_l(t) \quad \text{where} \quad w_l^* = \max_{s \in S(l)} (q_s^l(t) - q_s^{l_{next}}(t))$$



$$\hat{w}_l = \frac{w_l^*}{w_{l_{max}}}$$

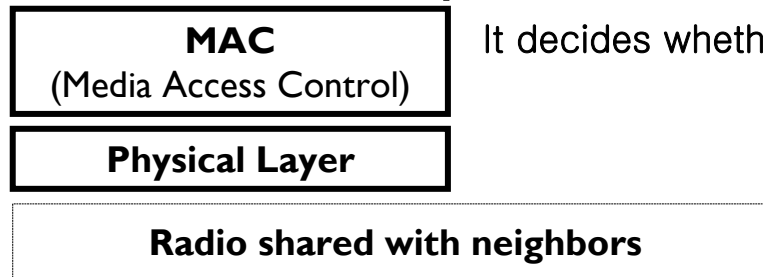
$w_{l_{max}}$  : Maximum weight value maintained link l

$$CW(\text{Contention Window}): \text{rand}((1 - \hat{w}_l)K, (1 - \hat{w}_l)K + C) \quad K = CW_{max} - C$$

Contention Window decides independently

# Overlay MAC Implementation

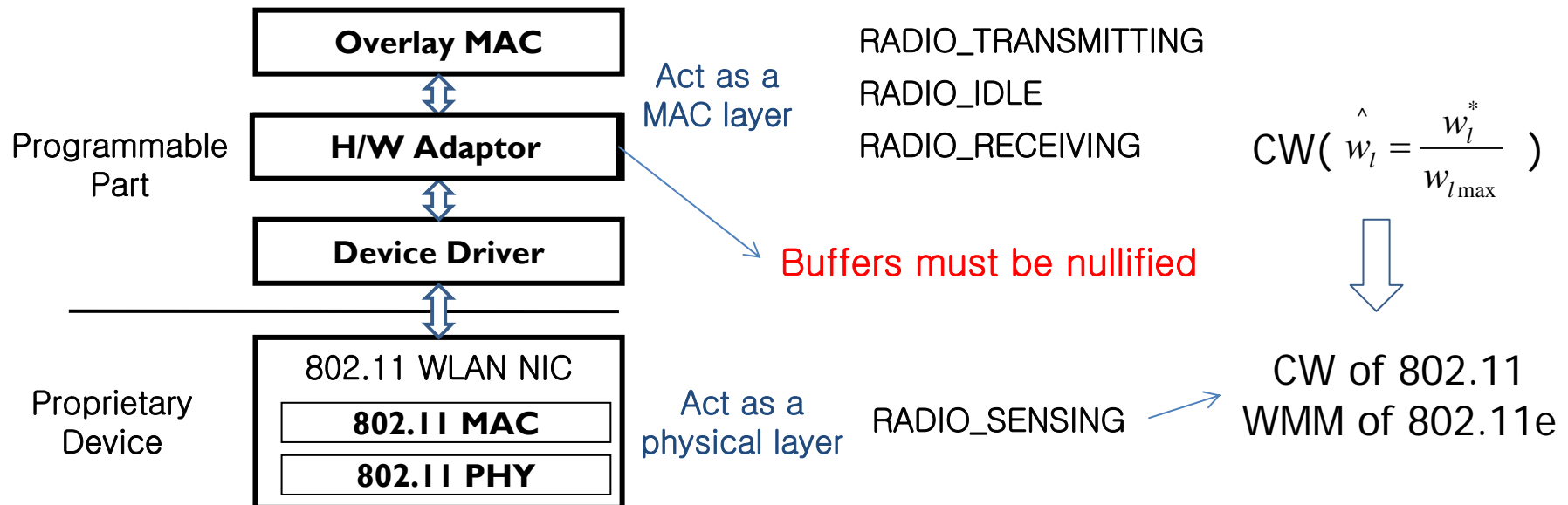
## ▶ Radio Status Requirement for MAC Operation



It decides whether transmitting or not

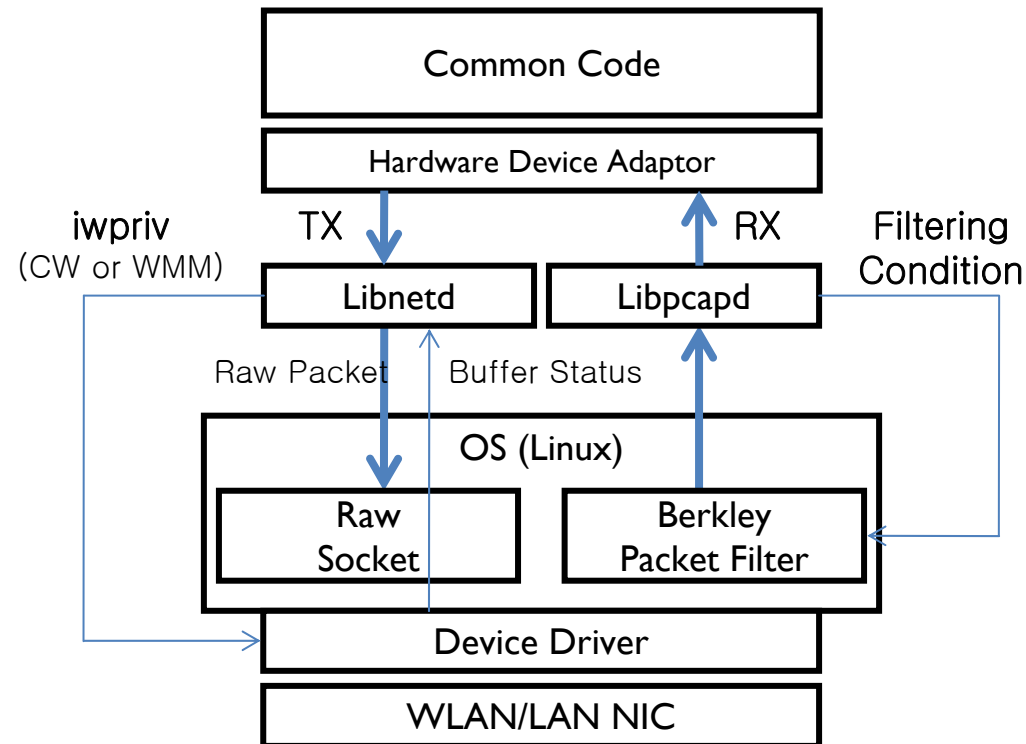
- RADIO\_TRANSMITTING
- RADIO\_IDLE
- RADIO\_RECEIVING
- RADIO\_SENSING

## ▶ Overlay MAC Design



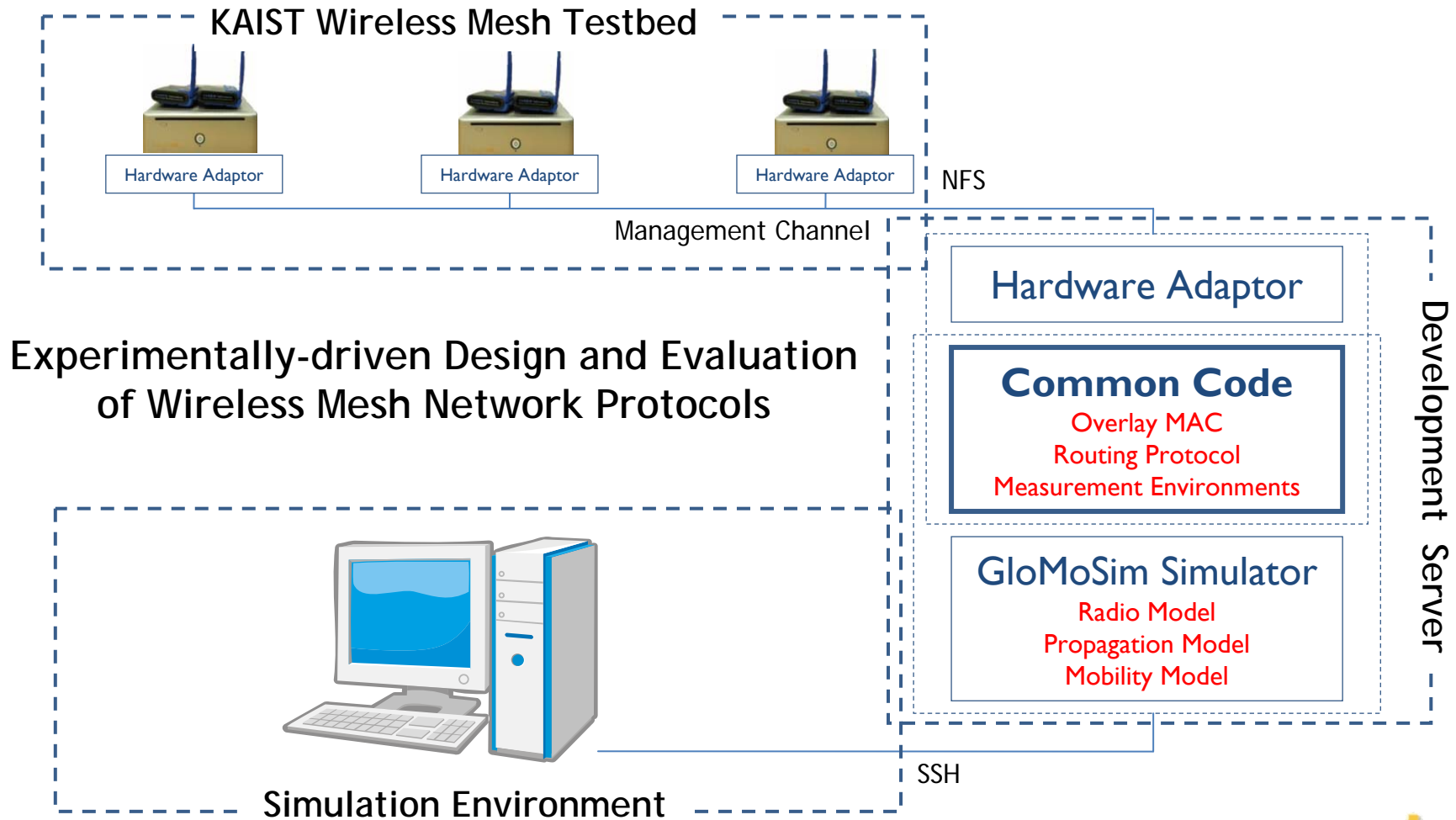
# Hardware Device Adaptor

## ▶ RT2570(Ralink) / AR5212(Atheros)



# Experimentally-driven Design and Evaluation

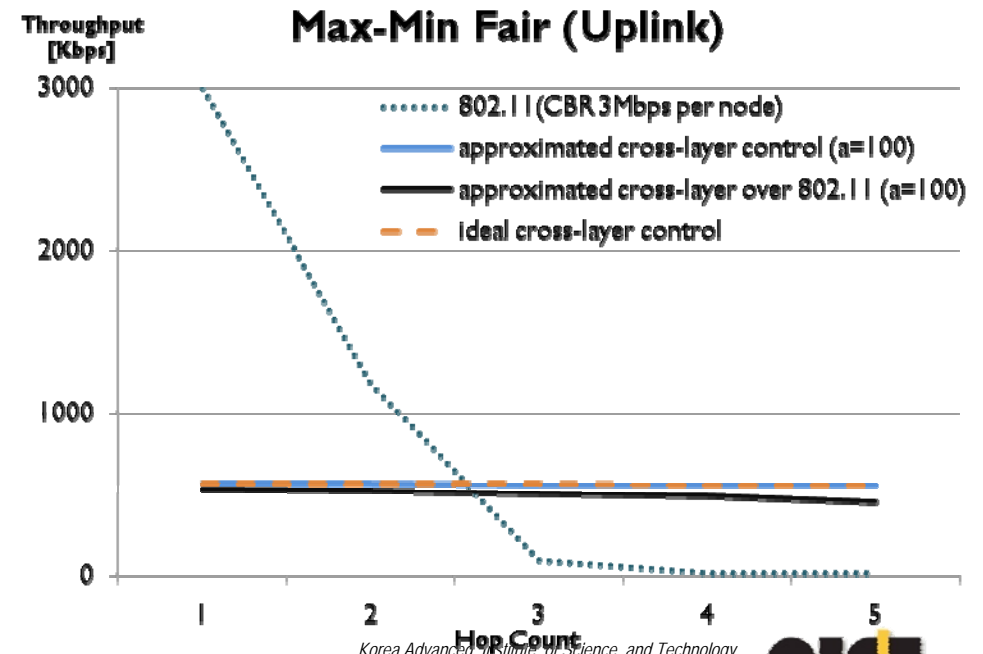
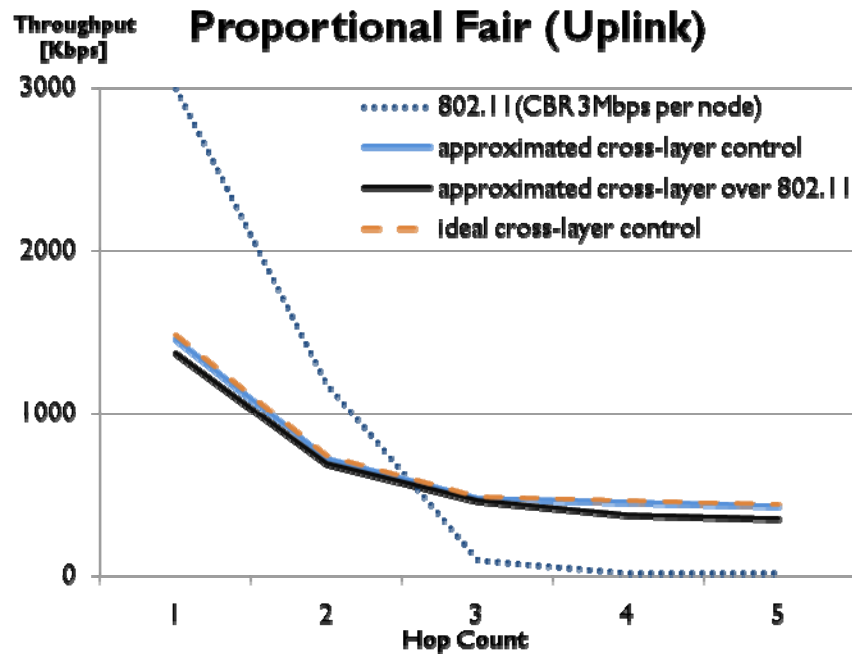
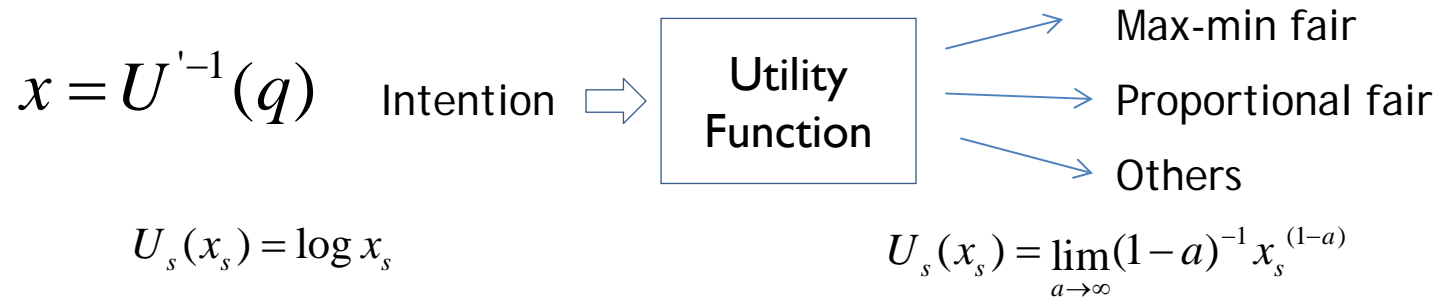
## ▶ Co-Simulation and Emulation Development Environment



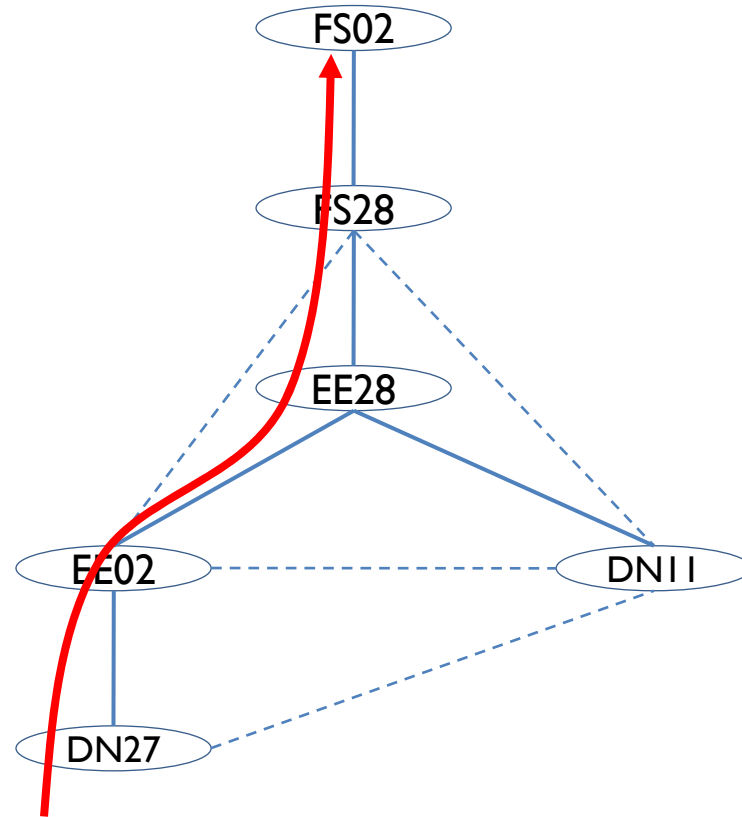
Experimentally-driven Design and Evaluation  
of Wireless Mesh Network Protocols

# Simulation Results

## ▶ Network Engineering by Utility Function

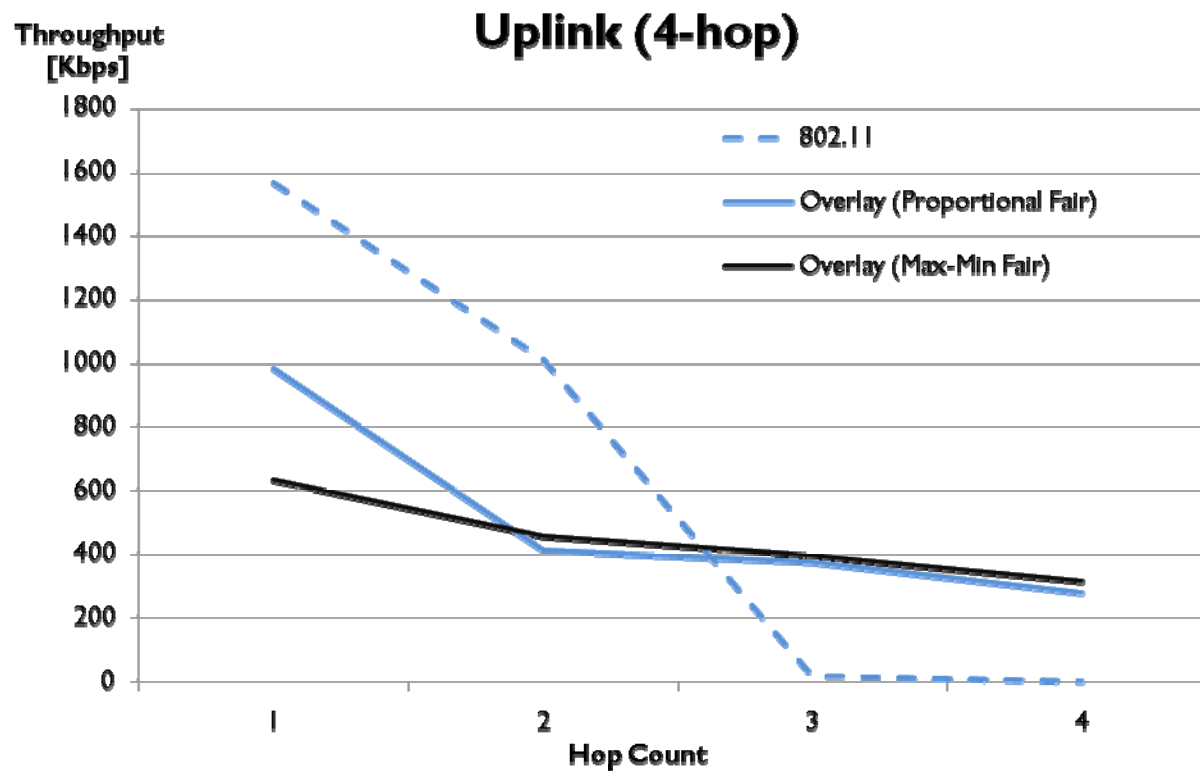


# KAIST Wireless Mesh Testbed



# Experimentation

## ▶ Network Engineering by Utility Function



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- ▶ [QualNet] <http://www.scalable-networks.com/>
- ▶ [OPNET] <http://www.opnet.com/>
- ▶ [WiMesh] <http://143.248.238.6/~sachin/realization/>
- ▶ [RoofNet] <http://pdos.csail.mit.edu/roofnet/docu.php>